

# MEASURING PERFORMANCE

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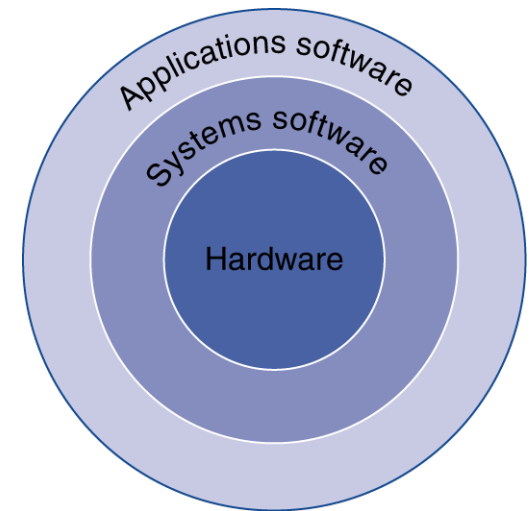
# Overview

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- This lecture
  - ▣ Levels of program code
  - ▣ Chips process
  - ▣ Performance
  - ▣ Design principles

# Below Your Program

- Application software
  - ▣ Written in high-level language
  
- System software
  - ▣ Compiler: translates HLL code to machine code
  - ▣ Operating System: service code
    - Handling input/output
    - Managing memory and storage
    - Scheduling tasks and sharing resources
  
- Hardware
  - ▣ Processor, memory, I/O controllers



# Levels of Program Code

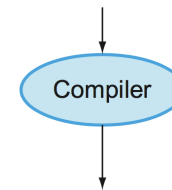
- High-level language
  - ▣ Level of abstraction closer to problem domain
  - ▣ Provides for productivity and portability

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swap(int v[], int k)
{int temp;
  temp = v[k];
  v[k] = v[k+1];
  v[k+1] = temp;
}
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# Levels of Program Code

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- Hardware representation
  - ▣ Binary digits (bits)
  - ▣ Encoded instructions and data

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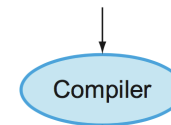


```
swap:
  multi $2, $5,4
  add $2, $4,$2
  lw $15, 0($2)
  lw $16, 4($2)
  sw $16, 0($2)
  sw $15, 4($2)
  jr $31
```

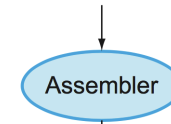
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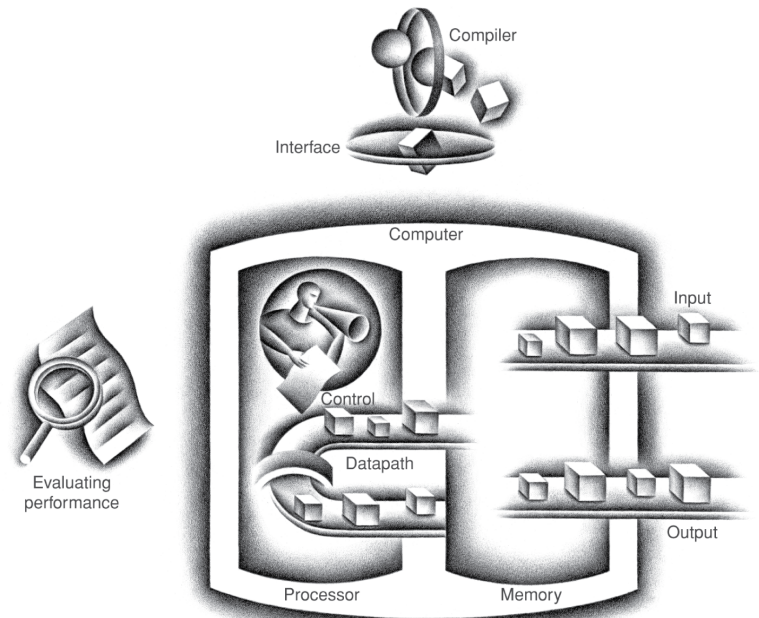
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  lw    $15, 0($2)
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  sw    $16, 0($2)
  sw    $15, 4($2)
  jr    $31
```



```
000000001010001000000000100011000
00000000100000100001000000100001
10001101111000100000000000000000
100011100001001000000000000000100
101011100001001000000000000000000
101011011110001000000000000000100
00000011111000000000000000001000
```

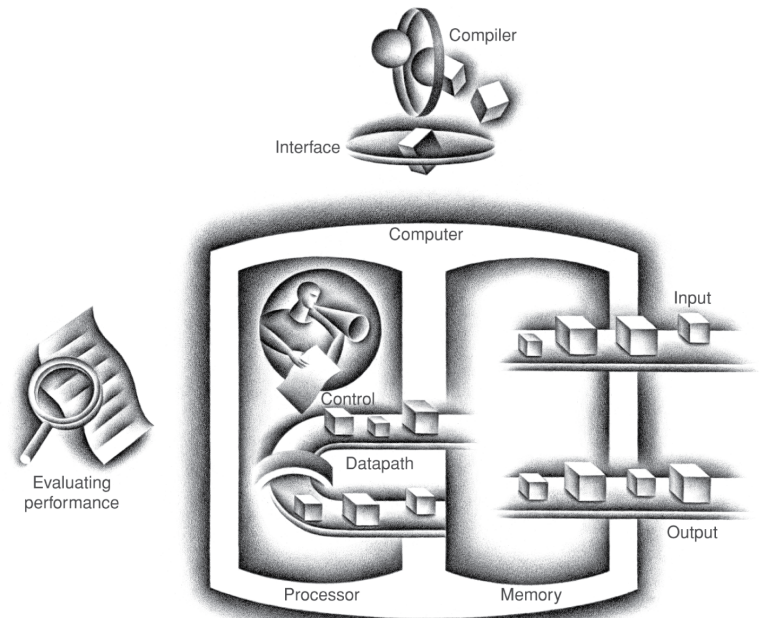
# Components of Computer

- Same components for all kinds of computer
  - ▣ Desktop, server, embedded
  
- Input/output includes
  - ▣ User-interface devices
    - Display, keyboard, mouse
  
  - ▣ Storage devices
    - Hard disk, CD/DVD, flash
  
  - ▣ Network adapters
    - For communicating with other computers



# Inside Processor (CPU)

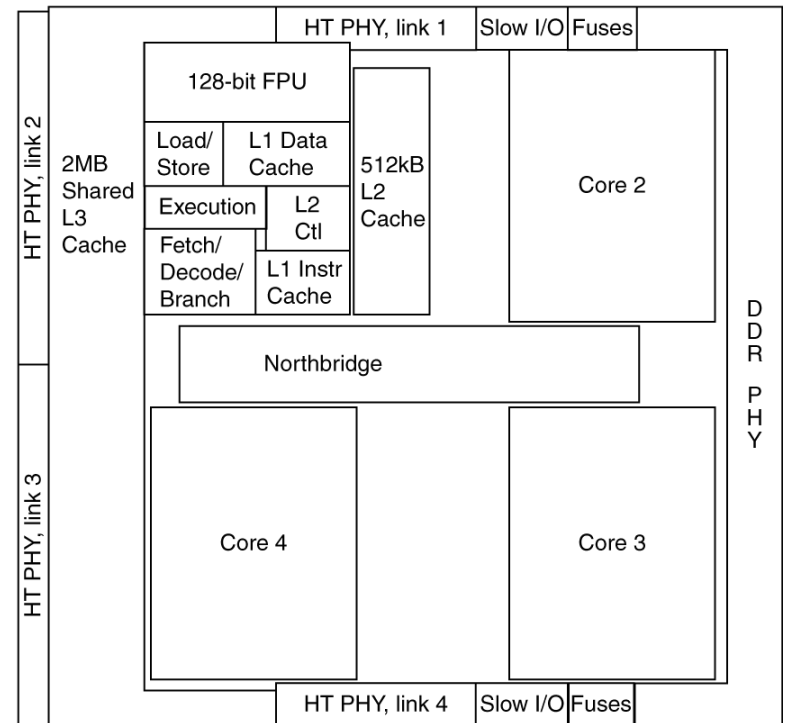
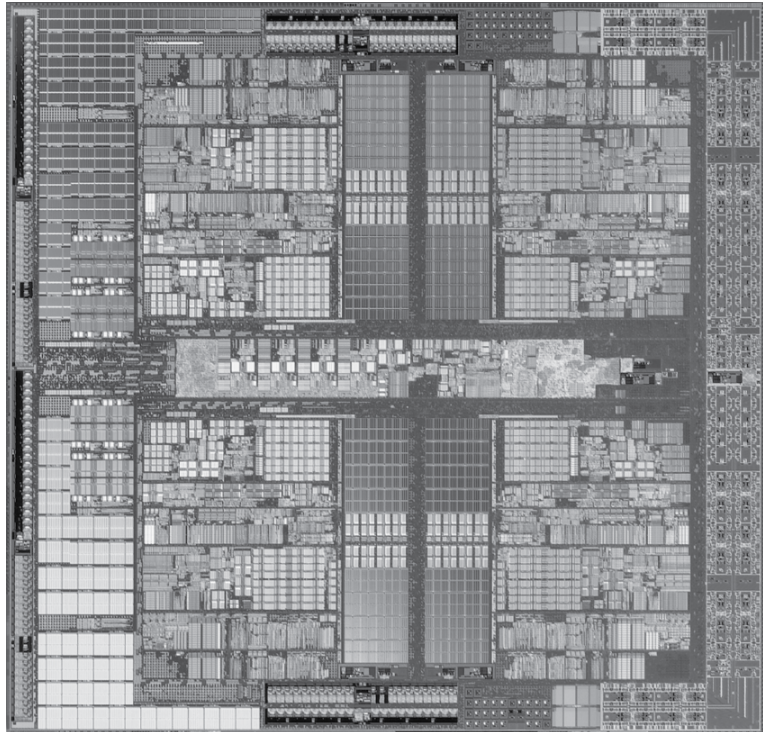
- Datapath
  - ▣ performs operations on data
- Control
  - ▣ sequences datapath, memory, ...
- Cache memory
  - ▣ Small fast SRAM memory for immediate access to data



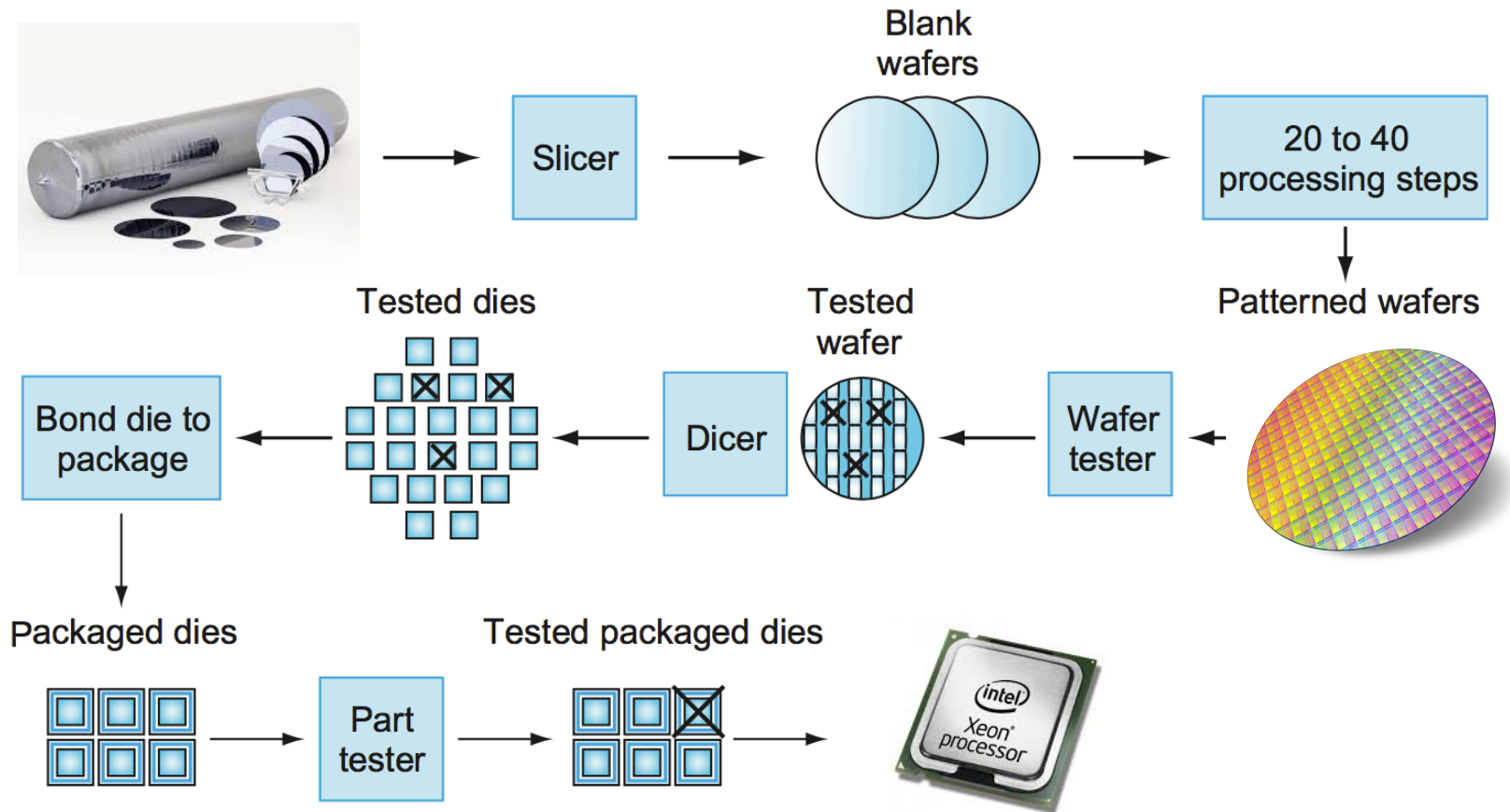


# Inside Processor (CPU)

- AMD Barcelona: four processor cores



# The Chip Manufacturing Process



# Measuring Performance

- How to measure performance?
  - ▣ Latency or response time
    - The time between start and completion of an event (e.g., milliseconds for disk access)
  - ▣ Bandwidth or throughput
    - The total amount of work done in a given time (e.g., megabytes per second for disk transfer)

# Measuring Performance

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- Which one is better? latency or throughput?

# Measuring Performance

- Which one is better (faster)?

Car

- Delay=10m
- Capacity=4p

Bus

- Delay=30m
- Capacity=30p

# Measuring Performance

- Which one is better (faster)?

Car

- Delay=10m
- Capacity=4p
- Throughput=0.4PPM

Bus

- Delay=30m
- Capacity=30p
- Throughput=1PPM

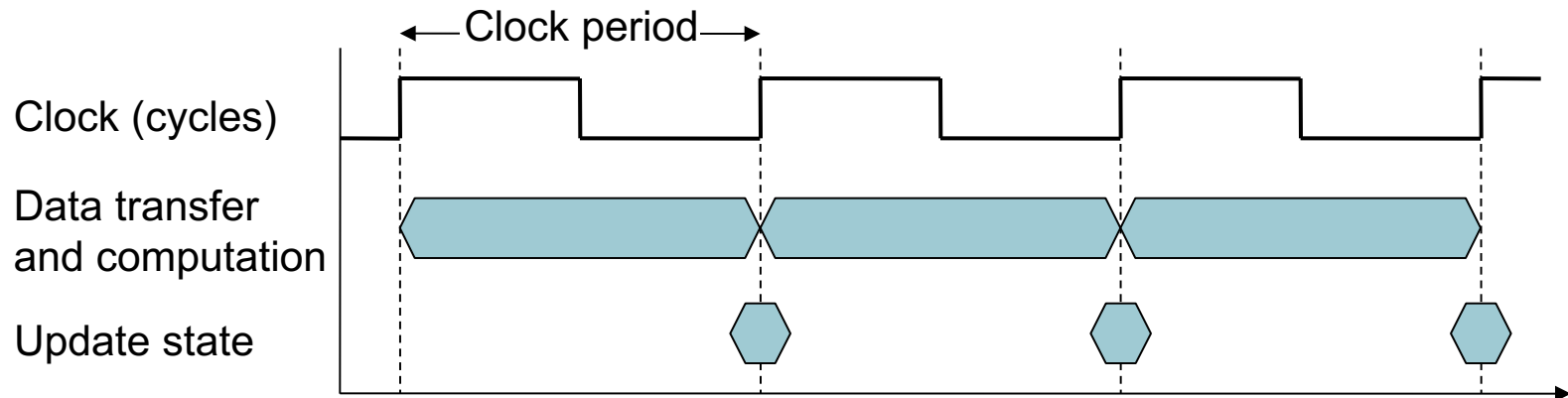
**It really depends on your needs (goals).**

# Measuring Execution Time

- Elapsed time
  - ▣ Total response time, including all aspects
  - ▣ Processing, I/O, OS overhead, idle time
  - ▣ Determines system performance
- CPU time
  - ▣ Time spent processing a given job
  - ▣ Discounts I/O time, other jobs' shares
  - ▣ Comprises user CPU time and system CPU time
  - ▣ Different programs are affected differently by CPU and system performance

# Clocking and Cycle Time

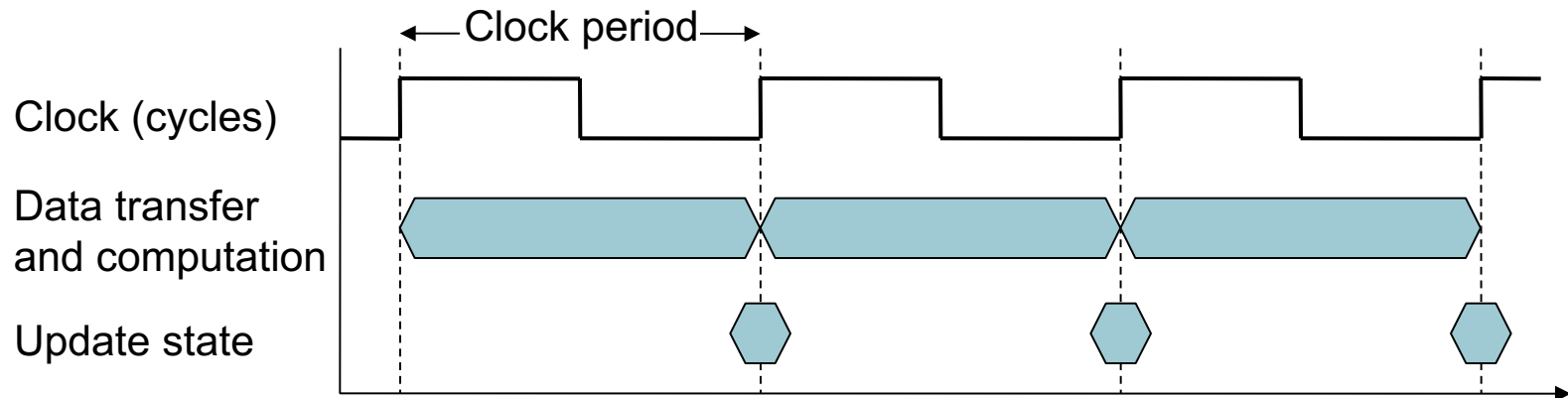
- Operation of digital hardware governed by a constant-rate clock





# Clocking and Cycle Time

- Operation of digital hardware governed by a constant-rate clock



- Clock period: duration of a clock cycle
  - ▣ e.g.,  $250\text{ps} = 0.25\text{ns} = 250 \times 10^{-12}\text{s}$
- Clock frequency (rate): cycles per second
  - ▣ e.g.,  $4.0\text{GHz} = 4000\text{MHz} = 4.0 \times 10^9\text{Hz}$

# The Processor Performance

- Clock cycle time ( $CT = 1/\text{clock frequency}$ )
  - ▣ Influenced by technology and pipeline
- Cycles per instruction (CPI)
  - ▣ Influenced by architecture
  - ▣ IPC may be used instead ( $IPC = 1/CPI$ )
- Instruction count (IC)
  - ▣ Influenced by ISA and compiler
- CPU time =  $IC \times CPI \times CT$
- Performance =  $1/\text{Execution Time}$

# Speedup vs. Percentage

- $\text{Speedup} = \text{old execution time} / \text{new execution time}$
- $\text{Improvement} = (\text{new performance} - \text{old performance}) / \text{old performance}$
- My old and new computers run a particular program in 80 and 60 seconds; compute the followings
  - ▣ speedup
  - ▣ percentage increase in performance
  - ▣ reduction in execution time

# Speedup vs. Percentage

- Speedup = old execution time / new execution time
- Improvement = (new performance - old performance) / old performance
- My old and new computers run a particular program in 80 and 60 seconds; compute the followings
  - ▣ speedup =  $80/60$
  - ▣ percentage increase in performance =  $33\%$
  - ▣ reduction in execution time =  $20/80 = 25\%$

# Principles of Computer Design

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- Designing better computer systems requires better utilization of resources
  - ▣ Parallelism
    - Multiple units for executing partial or complete tasks
  - ▣ Principle of locality (temporal and spatial)
    - Reuse data and functional units
  - ▣ Common Case
    - Use additional resources to improve the common case